

**REMARKS**

Claims 1-20 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding rejections are respectfully requested in light of the above amendments and following remarks.

Claims 1-20 were rejected under 35 U.S.C. § 112, second paragraph, as being indefinite.

This rejection is traversed.

The Examiner alleges that it is misdescriptive that the output signal controls the processor "only" during startup.

Claim 1 recites that the processor is controlled during startup by the output signal.

The word "only" does not appear in Claim 1.

Turning now to the art rejections, Claims 1-20 were rejected under 35 U.S.C. § 102(b) as being anticipated by Nagano.

It is respectfully submitted that Nagano does not disclose or suggest the presently claimed invention including the processor being controlled during startup by the output signal in independent Claim 1, the processor being controlled during startup by the control signal in independent Claim 9, albeit defined as the step of controlling the processor during startup by the control signal in independent Claim 17.

The Examiner alleges that the processor device is intended use.

A rejection based on intent use is only valid when the phrase appears only in the preamble.

Here, the processor is in the body of the claim language and the intended use rejection is not properly applied.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



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